

TITLE: PARTIAL PAGE PROGRAMMING OF MULTI LEVEL FLASH INVENTOR(S): Allan Parker; Glen Larn Attorney Docket #: AMD-G1255

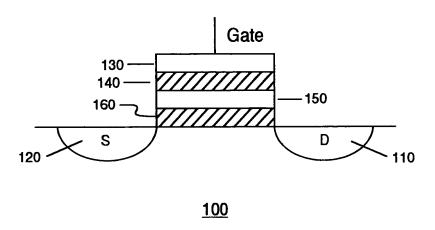


Figure 1

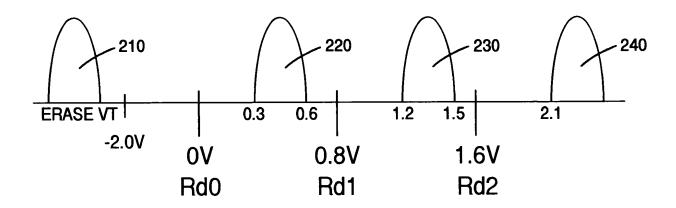


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200

Figure 2



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LEVEL	Q2	Q1
2	0	0
1	0	1
0	1	0
Erase	1	1

<u>300</u>

Figure 3



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LEVEL	Q2	Q1]
2	0	0	←
1	0	1	- \ ,
0	1	0	410 - 42
Erase	1	1	
	3	00	

Figure 4A



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LEVEL	Q2	Q1	
2	0	0	~
1	0	1)— 430
0	1	0	
Erase	1	1	
	3	00	

Figure 4B



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LEVEL	Q2	Q1]
2	0	0	<
1	0	1	
0	1	0	1
Erase	1	1]
	3	<u> 800</u>	

Figure 4C



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<u>500</u>

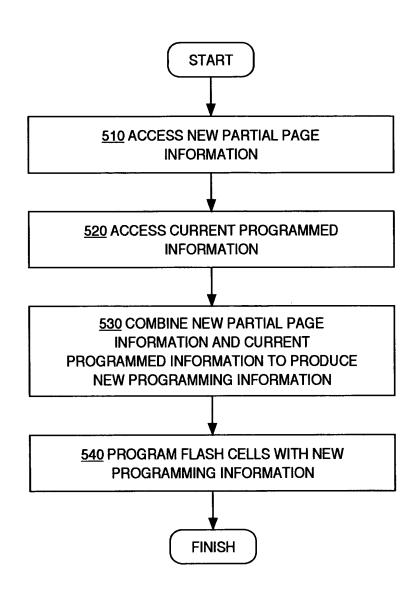


Figure 5

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600

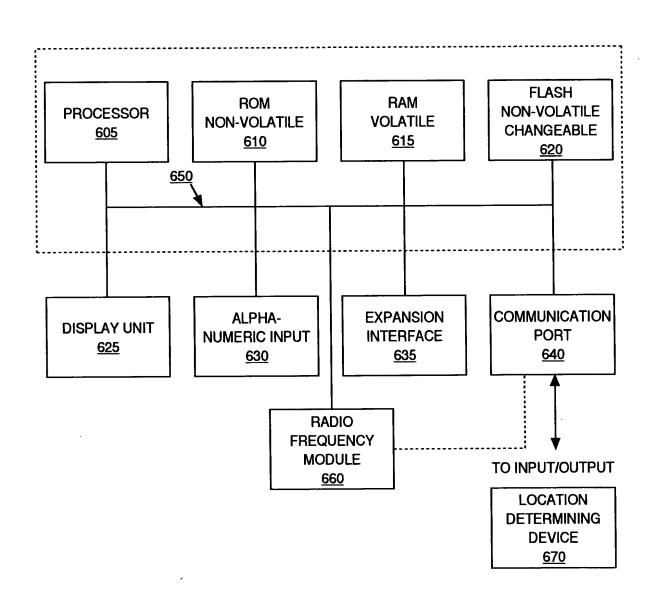


Figure 6